



8/10 2116 ✓  
**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In the **PATENT APPLICATION** of:

Gredone et al.

**Application No.:** 09/990,060

**Confirmation No.:** 1472

**Filed:** November 21, 2001

**For:** HYBRID PARALLEL/SERIAL BUS  
INTERFACE

**Group:** 2116

**Examiner:** Patel, Nitin C.

**Our File:** I-2-0201US

**Date:** March 3, 2005

**REPLY PURSUANT TO 37 C.F.R. §1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This Reply and Terminal Disclaimer are being timely filed in response to the  
Office Action dated December 7, 2004.

Please amend the application without prejudice or disclaimer as follows: